

WEST Search History

DATE: Thursday, June 07, 2007

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	<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L20	l17 and (quality or performance)	2
<input type="checkbox"/>	L19	l17 and (consumption or sav\$3 or conserv\$5 or reduc\$5 or minim\$5)	2
<input type="checkbox"/>	L18	l17 and power	2
<input type="checkbox"/>	L17	(6222550 or 6473089 or 6384833).pn.	3
<input type="checkbox"/>	L16	l11 and (l4 same (class\$6 or discriminat\$4 or catego\$))	1
<input type="checkbox"/>	L15	l12 and (class\$6 or discriminat\$4 or catego\$)	46
<input type="checkbox"/>	L14	L12 and (sacrif\$ or tolerat\$4 or compromis\$4)	18
<input type="checkbox"/>	L13	L12 and (sacrif\$ or tolerat\$4 or compromis\$4)	10
<input type="checkbox"/>	L12	L11 and l4	226
<input type="checkbox"/>	L11	l6 or l7 or l8 or l9 or l10	15902
<input type="checkbox"/>	L10	345/212	1387
<input type="checkbox"/>	L9	345/98	2919
<input type="checkbox"/>	L8	345/89	2395
<input type="checkbox"/>	L7	345/87	6511
<input type="checkbox"/>	L6	(713/3\$2)! [CCLS]	6438
<input type="checkbox"/>	L5	L4 same ((class\$ or type) near5 data)	4
<input type="checkbox"/>	L4	display\$3 near7 (power (consumption or sav\$3 or conserv\$5 or reduc\$5 or minim\$5)) near7 (quality or performance)	1371
<input type="checkbox"/>	L3	l2 and ((class\$ or type) near5 data)	0
<input type="checkbox"/>	L2	6678834[uref]	4
<input type="checkbox"/>	L1	6678834.pn.	1

END OF SEARCH HISTORY

Day : Thursday

Date: 6/7/2007
Time: 23:21:42**Inventor Name Search Result**

Your Search was:

Last Name = HOLMER

First Name = BRUCE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>08928252</u>	6128641	150	09/12/1997	DATA PROCESSING UNIT WITH HARDWARE ASSISTED CONTEXT SWITCHING CAPABILITY	HOLMER, BRUCE
<u>09140474</u>	6292845	150	08/26/1998	PROCESSING UNIT HAVING INDEPENDENT EXECUTION UNITS FOR PARALLEL EXECUTION OF INSTRUCTIONS OF DIFFERENT CATEGORY WITH INSTRUCTION HAVING SPECIFIC BITS INDICATING INSTRUCTION SIZE AND CATEGORY RESPECTIVELY	HOLMER, BRUCE
<u>09189111</u>	6434689	150	11/09/1998	DATA PROCESSING UNIT WITH INTERFACE FOR SHARING REGISTERS BY A PROCESSOR AND A COPROCESSOR	HOLMER, BRUCE
<u>09263161</u>	6466220	150	03/05/1999	GRAPHICS ENGINE ARCHITECTURE	HOLMER, BRUCE
<u>10667098</u>	Not Issued	71	09/18/2003	High quality and high performance three-dimensional graphics architecture for portable handheld devices	HOLMER, BRUCE
<u>11600975</u>	Not Issued	41	11/17/2006	High quality and high performance three-dimensional graphics architecture for portable handheld devices	HOLMER, BRUCE
<u>11601413</u>	Not Issued	41	11/17/2006	High quality and high performance three-	HOLMER, BRUCE

				dimensional graphics architecture for portable handheld devices	
<u>10288402</u>	Not Issued	71	11/04/2002	Interactive set-top box having a unified memory architecture	HOLMER, BRUCE K.
<u>10993679</u>	Not Issued	41	11/18/2004	Method and system for a texture-aware virtual memory subsystem	HOLMER, BRUCE K.
<u>09263454</u>	<u>6526583</u>	150	03/05/1999	INTERACTIVE SET-TOP BOX HAVING A UNIFIED MEMORY ARCHITECTURE	HOLMER, BRUCE K.
<u>09264140</u>	<u>6411334</u>	150	03/05/1999	ASPECT RATIO CORRECTION USING DIGITAL FILTERING	HOLMER, BRUCE K.
<u>09283771</u>	Not Issued	168	03/31/1999	ADVANCED MEMORY REDUCTION SYSTEM FOR IMAGE PROCESSING SYSTEMS	HOLMER, BRUCE K.

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	Search
	<input type="text" value="HOLMER"/>	<input type="text" value="BRUCE"/>	

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